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Total Number of Pages in This Submission

9

Application Number 10/645,040

Filing Date 8/21/2003

First Named Inventor Boerstler

Art Unit 2819

Examiner Name Vibol Tan

Attorney Docket Number UAS920030368US1 (IBM 2714000)

ENCLOSURES (Check all that apply)

<input type="checkbox"/> Fee Transmittal Form	<input type="checkbox"/> Drawing(s)	<input type="checkbox"/> After Allowance Communication to TC
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Remarks

Reply to Office Action dated December 13, 2004 (8 pages).

SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT

Firm Name	CARR LLP		
Signature			
Printed name	Gregory W. Carr		
Date	2/14/05	Reg. No.	31,093

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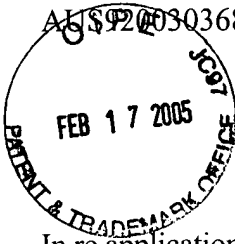
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ATTORNEY DOCKET NO.
AJS920030368US1 (IBM 2714000)

1FW
PATENT APPLICATION
SERIAL NO. 10/645,040



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Boerstler et al.

Serial Number: 10/645,040

Filed: August 21, 2003

For: CLOCK SIGNAL SELECTOR CIRCUIT
WITH REDUCED PROBABILITY OF
ERRONEOUS OUTPUT DUE TO
METASTABILITY

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§

Group Art Unit: 2819

Examiner: Vibol Tan

Mail Stop Amendment
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2/14/05

Bradley D. Ellis
Bradley D. Ellis

RESPONSE

Dear Sir:

In response to the Office Action dated December 13, 2004 ("Office Action"), Applicants
respond as follows: